

FIG. 1

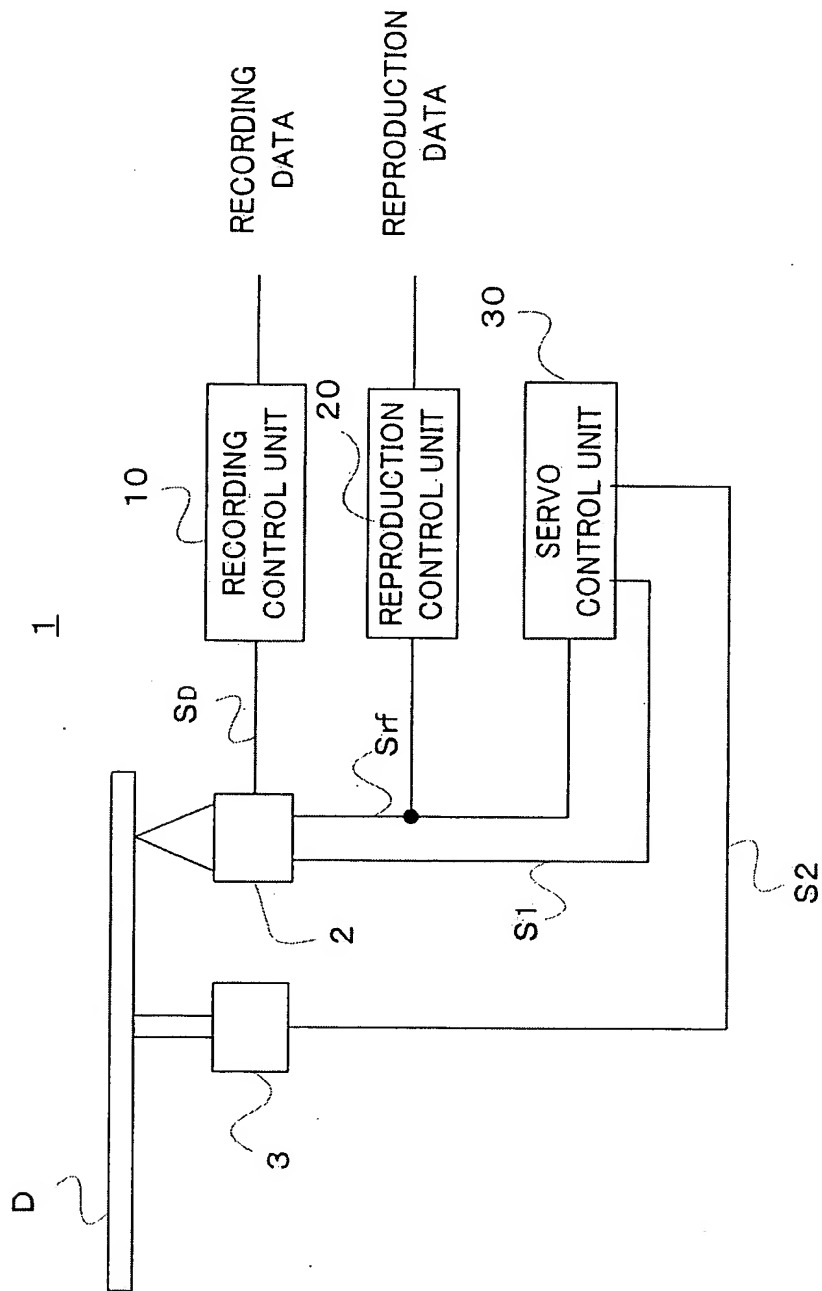


FIG. 2

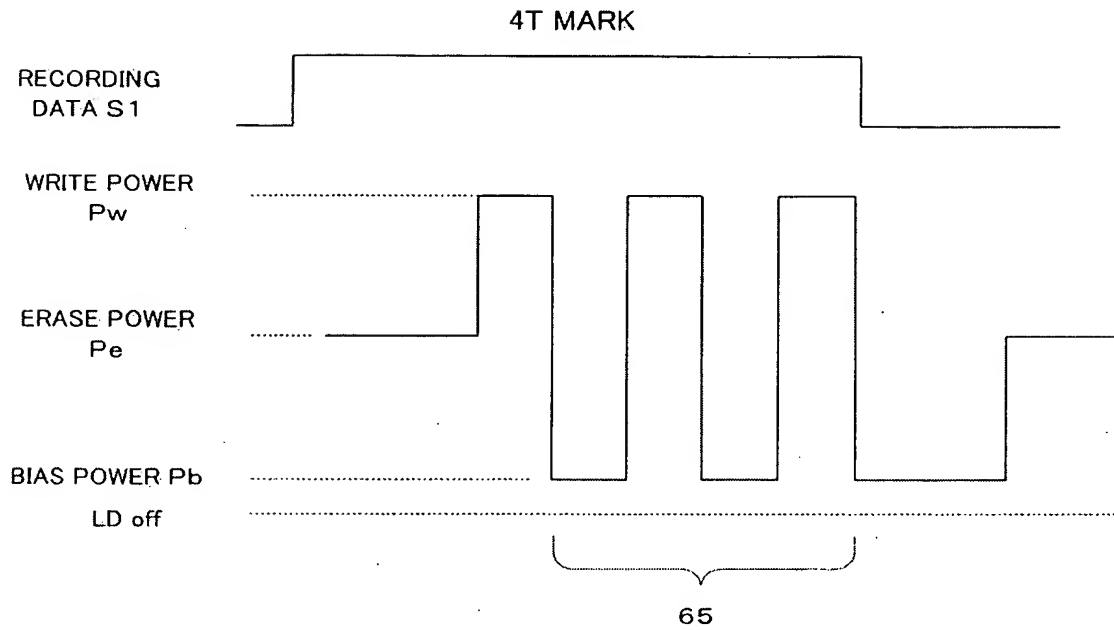


FIG. 3A

WRITE POWER P_w
ERASE POWER P_e
BIAS POWER P_b

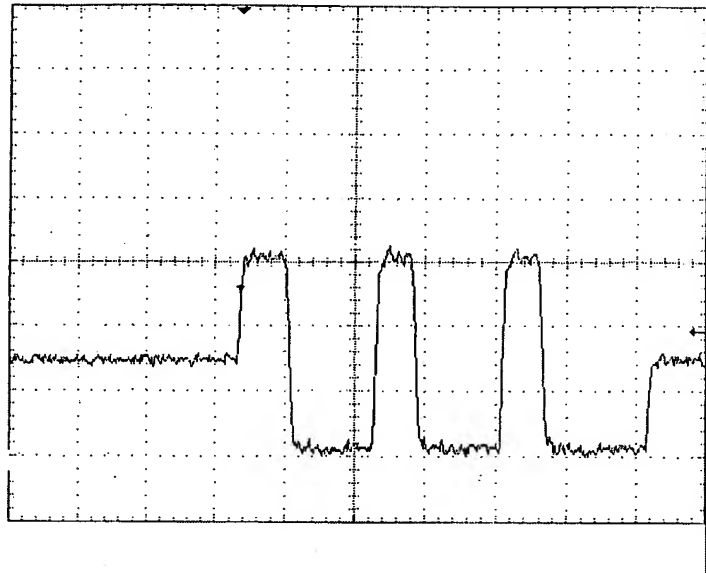


FIG. 3B

WRITE POWER P_w
ERASE POWER P_e
BIAS POWER P_b

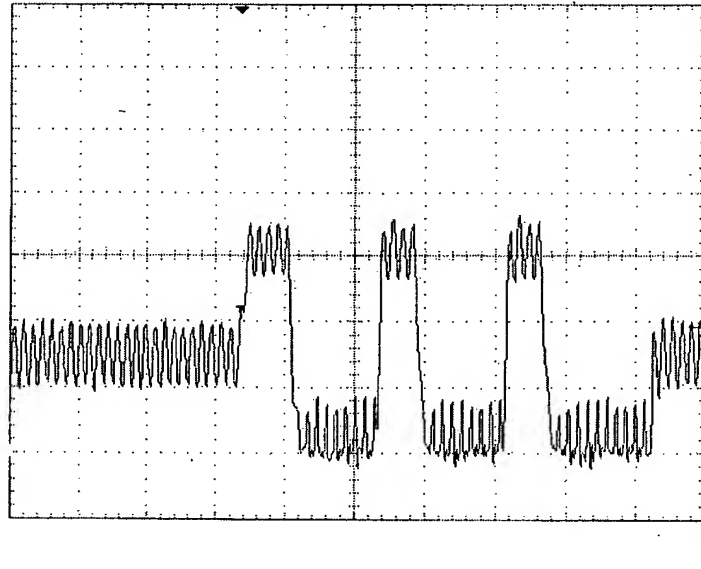


FIG. 4

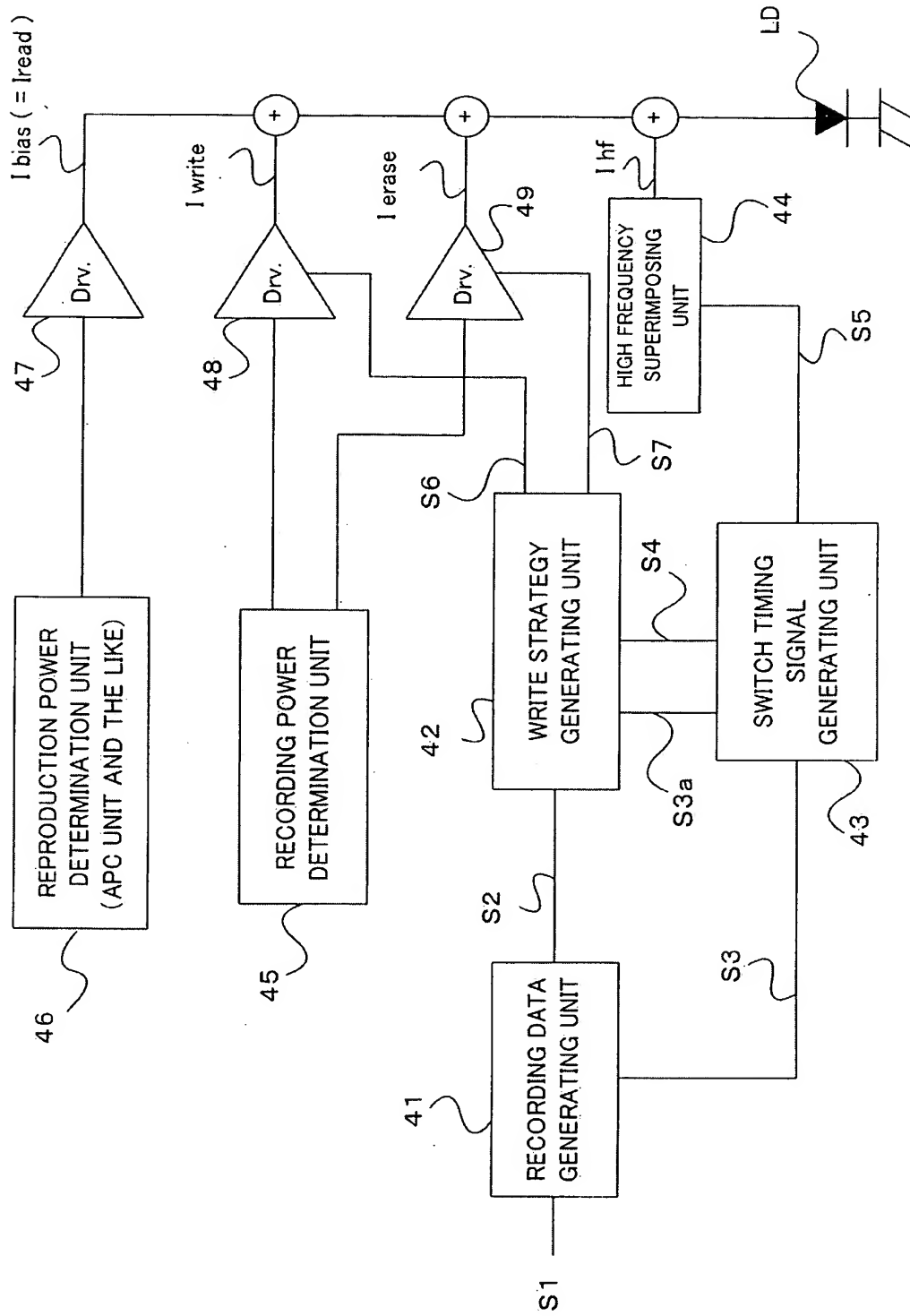


FIG. 5

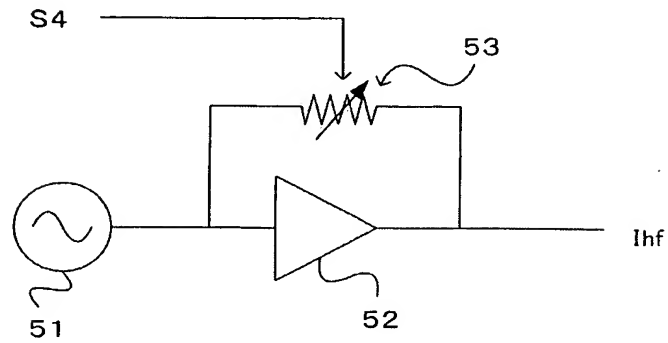


FIG. 6

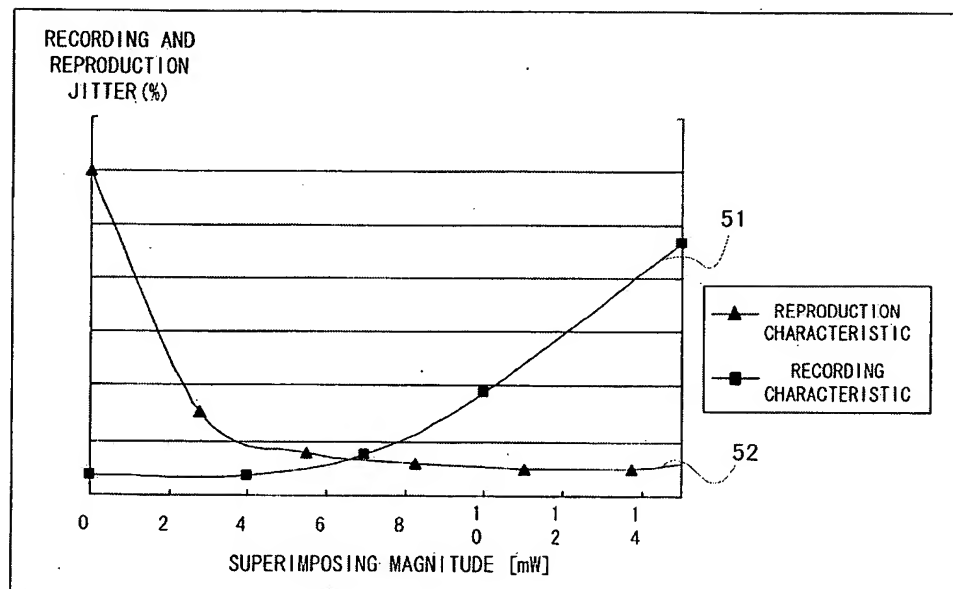


FIG. 7

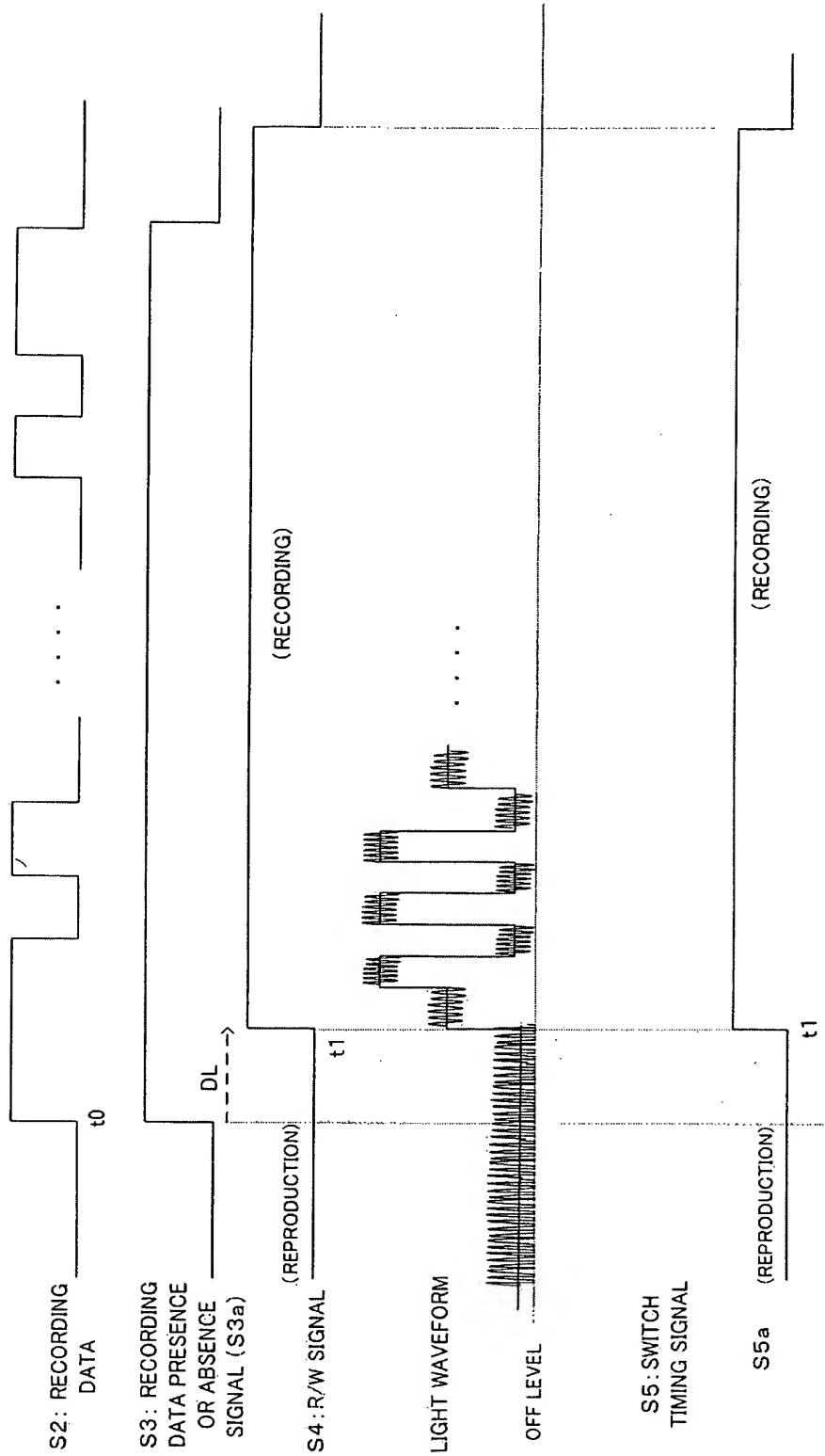


FIG. 8

